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CONFIRMATION NO. 3657

<b>SERIAL NUMBER</b> 10/534,946	<b>FILING OR 371(c) DATE</b> 05/11/2005 <b>RULE</b>	<b>CLASS</b> 117	<b>GROUP ART UNIT</b> 1722	<b>ATTORNEY DOCKET NO.</b> 94326
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**\*\* CONTINUING DATA \*\*\*\*\***

This application is a 371 of PCT/JP03/14509 11/14/2003 *m>*

**\*\* FOREIGN APPLICATIONS \*\*\*\*\***

JAPAN 2002-331288 11/14/2002 *m>*  
 JAPAN 2003-46411 02/24/2003 *m>*

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>[Signature]</i> Examiner's Signature Initials	<b>STATE OR COUNTRY</b> JAPAN	<b>SHEETS DRAWING</b> 25	<b>TOTAL CLAIMS</b> 21	<b>INDEPENDENT CLAIMS</b> 9
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**ADDRESS**  
24628

**TITLE**

Silicon wafer, its manufacturing method, and its manufacturing apparatus

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